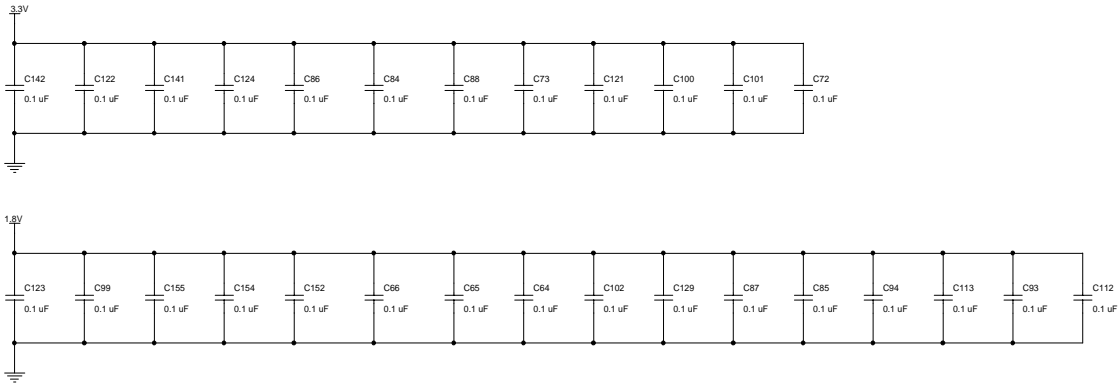
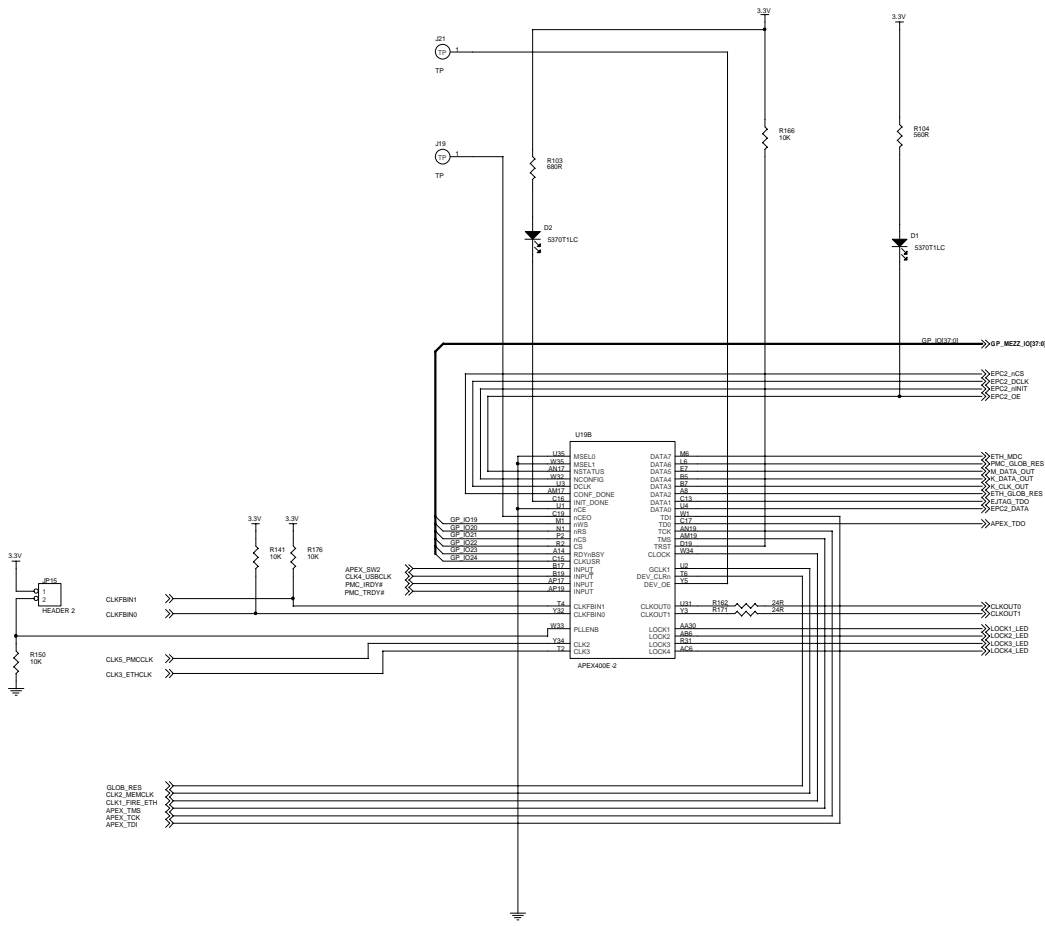


APEX400E-2



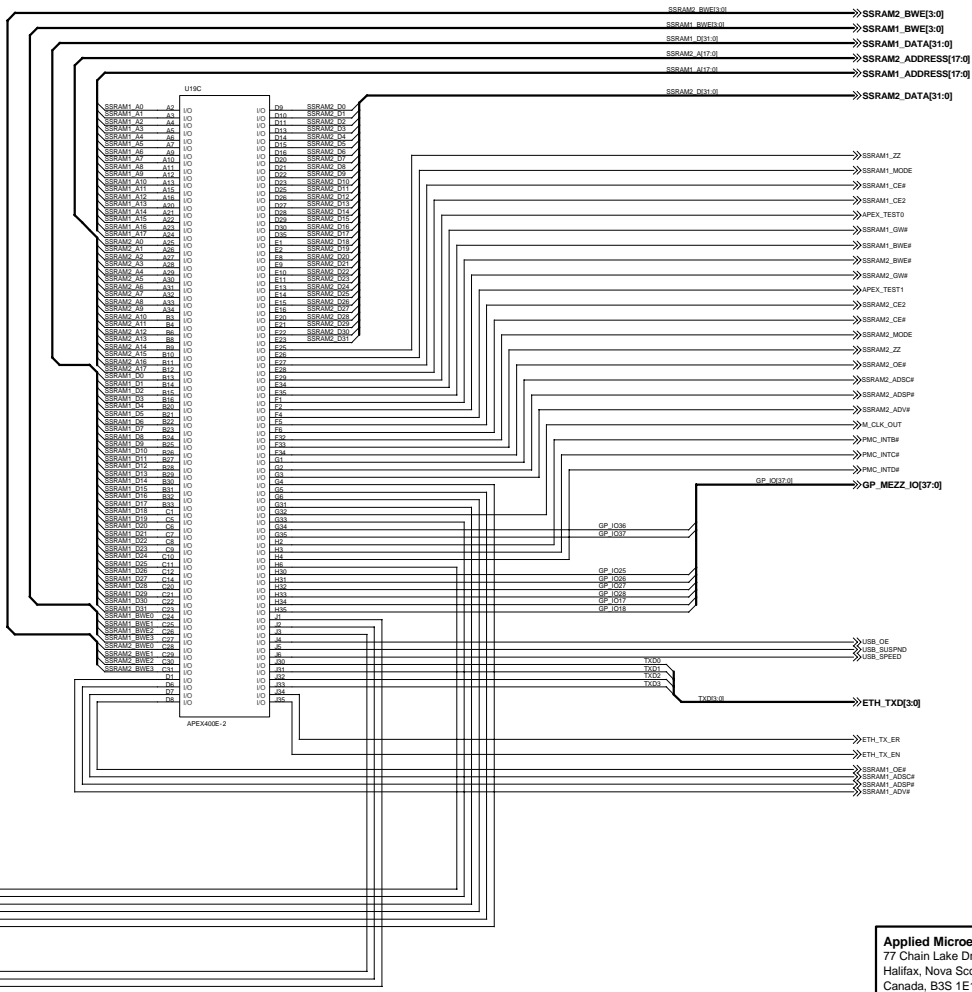
APEX PART A

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quinon		Checked By: Bruce Oakley	
Date: Tuesday, July 27, 1999		Release Date: Friday, January 14, 2000	
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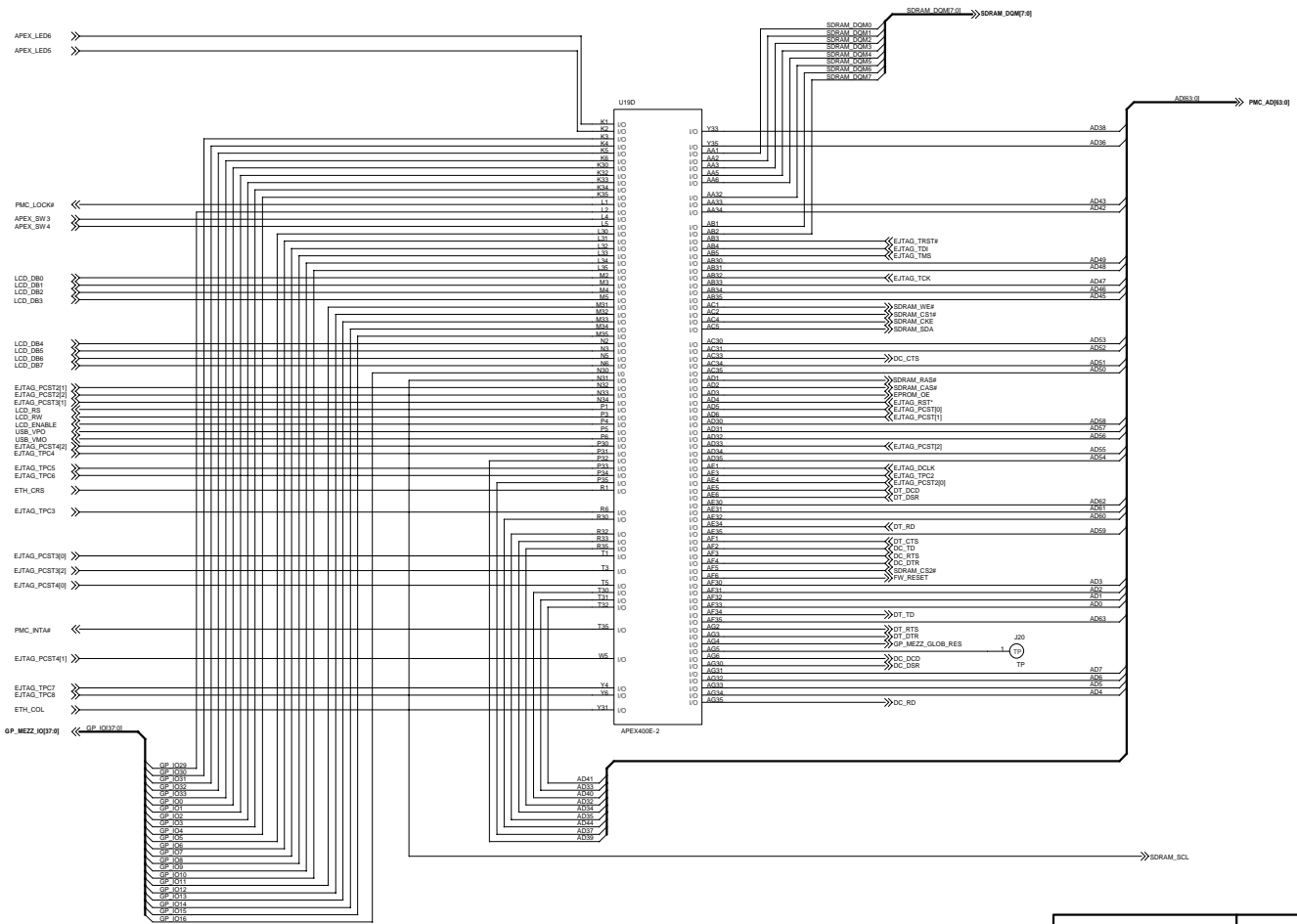
APEX PART B

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, July 27, 1999	Release Date: Friday, January 14, 2000		
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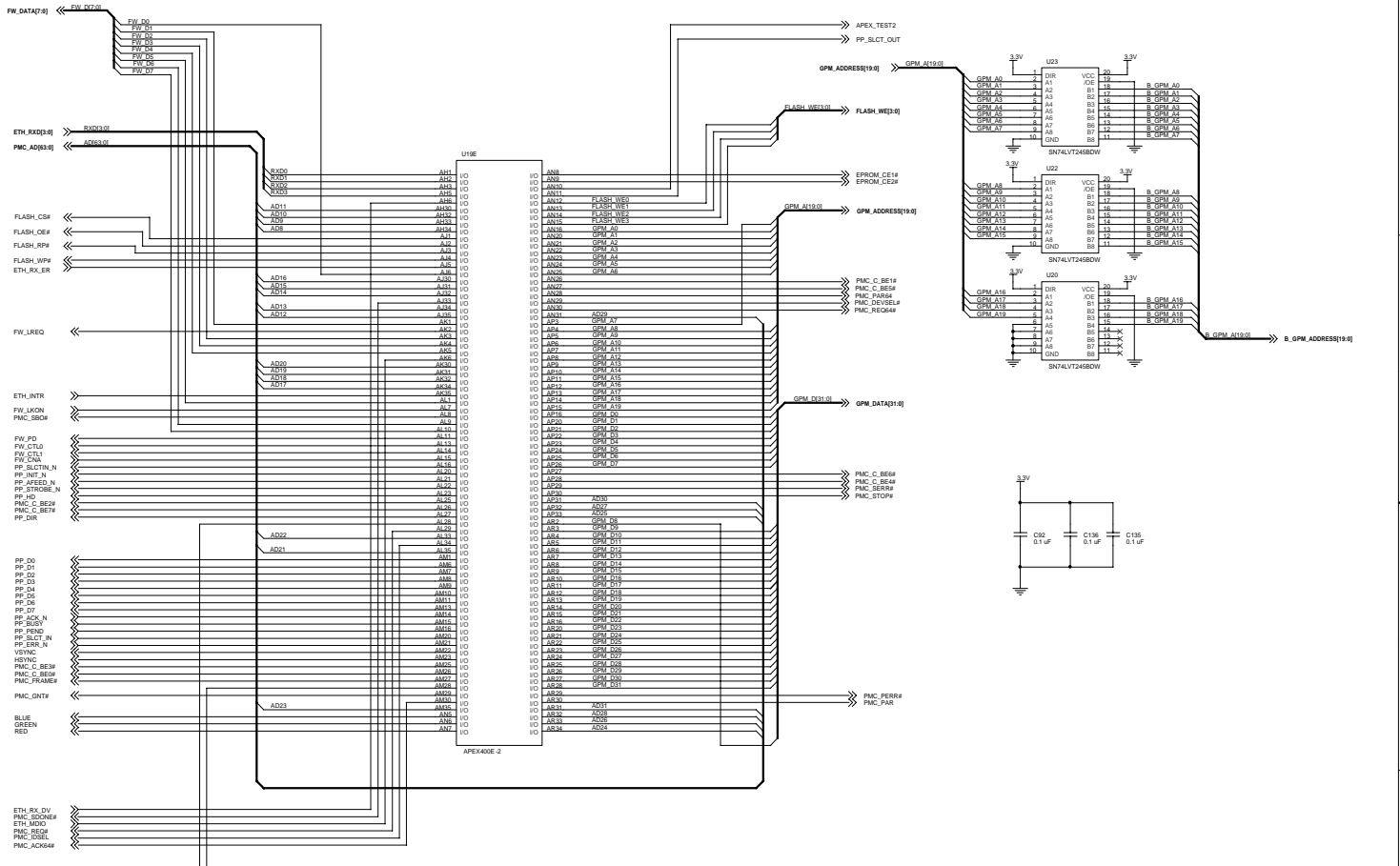
APEX PART C

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		Date: Tuesday, July 27, 1999
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APEX PART D

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, July 27, 1999	Release Date: Friday, January 14, 2000		
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APEX PART E

Applied Microelectronics Inc.  
 77 Chain Lake Drive  
 Halifax, Nova Scotia  
 Canada, B3S 1E1

Title: ALTERA SOPC  
 DEVELOPMENT BOARD

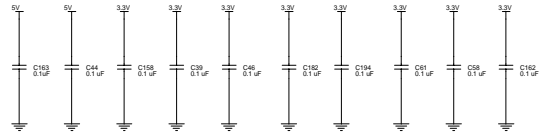
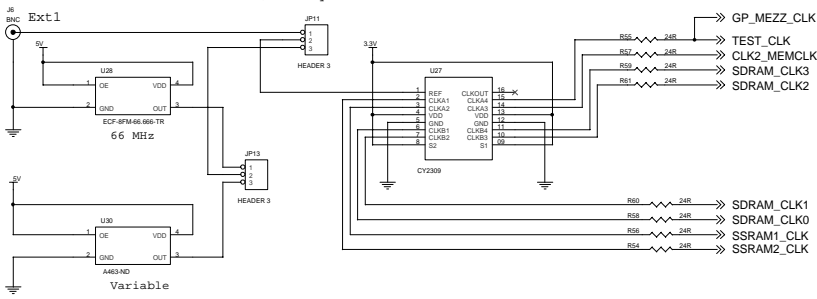
Document Number: SCH-002128-02 Rev: 2

Drawn By: Jean Quirion Checked By: Bruce Oakley

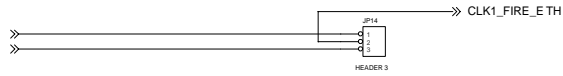
Date: Tuesday, July 27, 1999 Release Date: Friday, January 14, 2000

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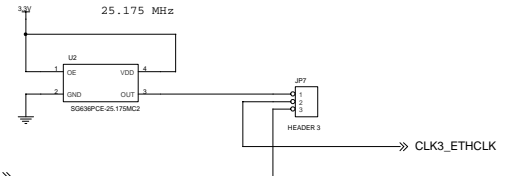
Processor / Memory Clock



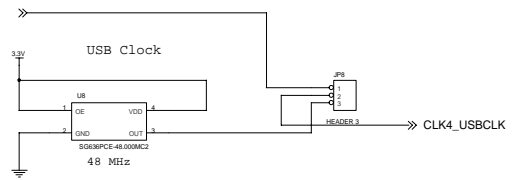
ETH\_TX\_CLK  
FWCLK



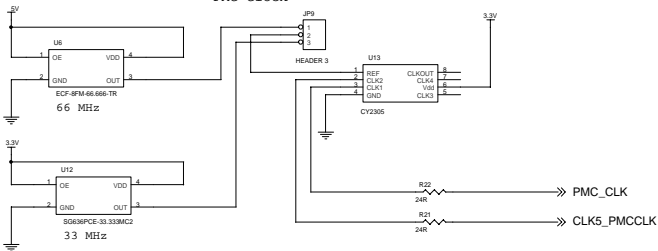
ETH\_RX\_CLK



EJTAG\_TCK



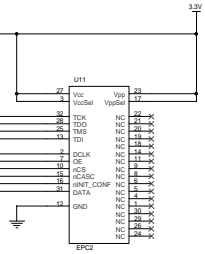
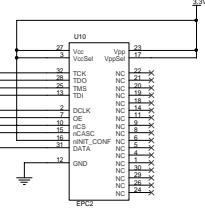
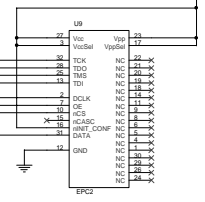
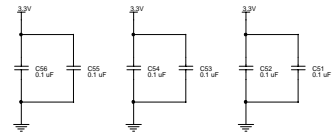
PMC Clock



<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC                  DEVELOPMENT BOARD</b>	
Document Number:	SCH-002128-02	Rev:	2
Drawn By:	Jean Quirion	Checked By:	Bruce Oakley
Date:	Tuesday, July 27, 1999	Release Date:	Friday, January 14, 2000
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CLOCK DISTRIBUTION

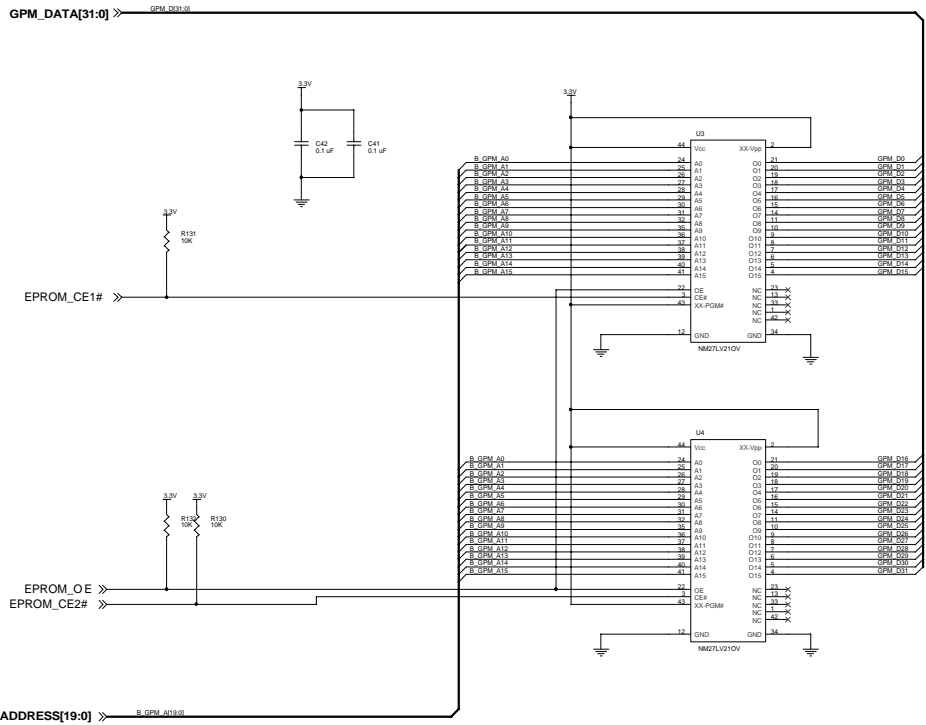
→EPC2\_TDO



EPC2\_TCK  
 EPC2\_TMS  
 EPC2\_TDI  
 EPC2\_DCLK  
 EPC2\_OE  
 EPC2\_nCS  
 EPC2\_nINIT  
 EPC2\_DATA

EPC2 INTERFACE

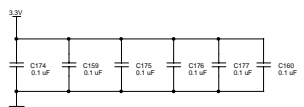
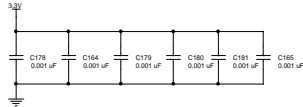
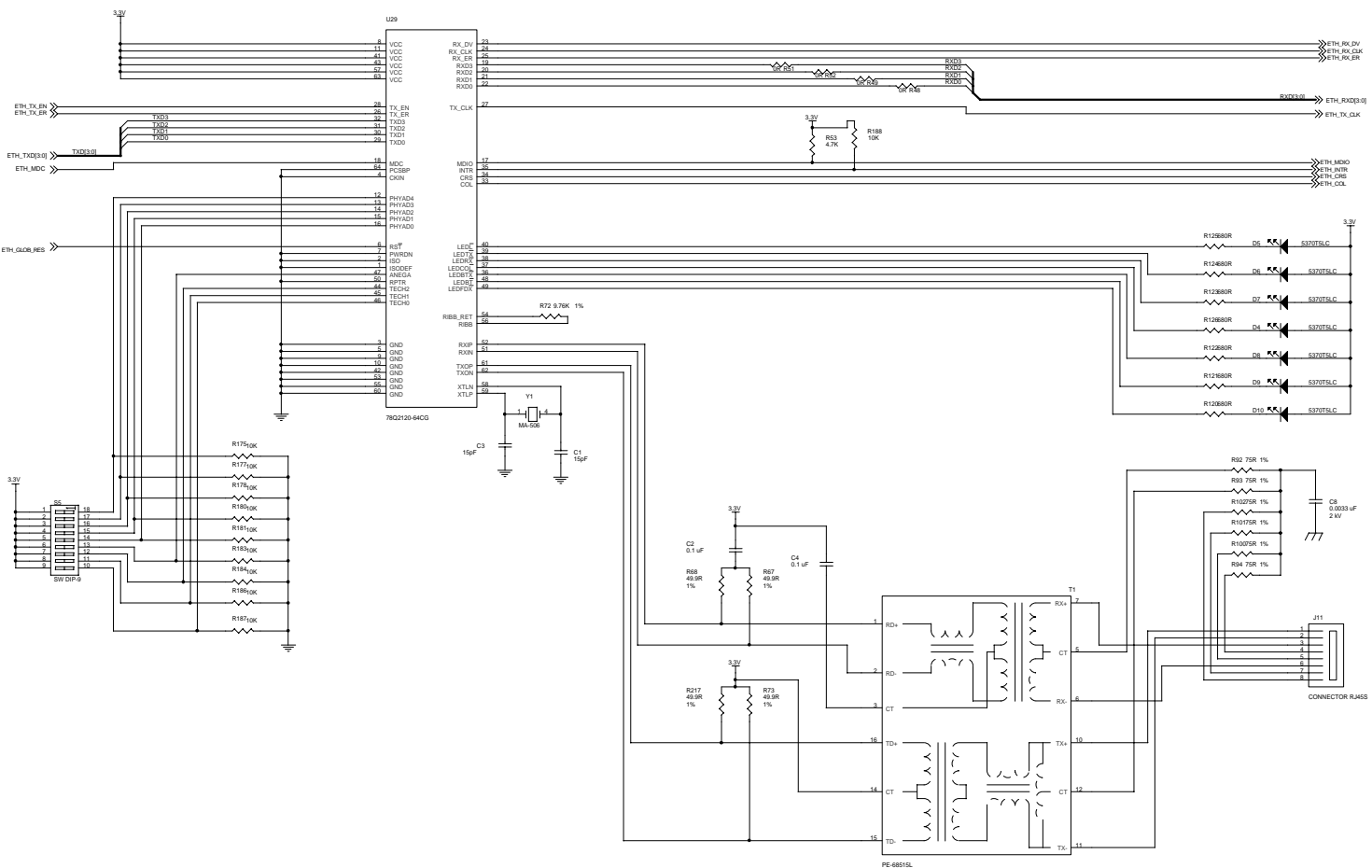
<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		Title: <b>ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, July 27, 1999	Release Date: Friday, January 14, 2000		
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<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number:	SCH-002128-02	Rev:	2
Drawn By:	Jean Quirion	Checked By:	Bruce Oakley
Date:	Tuesday, 209 27, 1999	Release Date:	Friday, January 14, 2000
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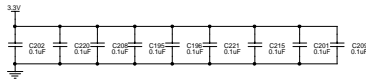
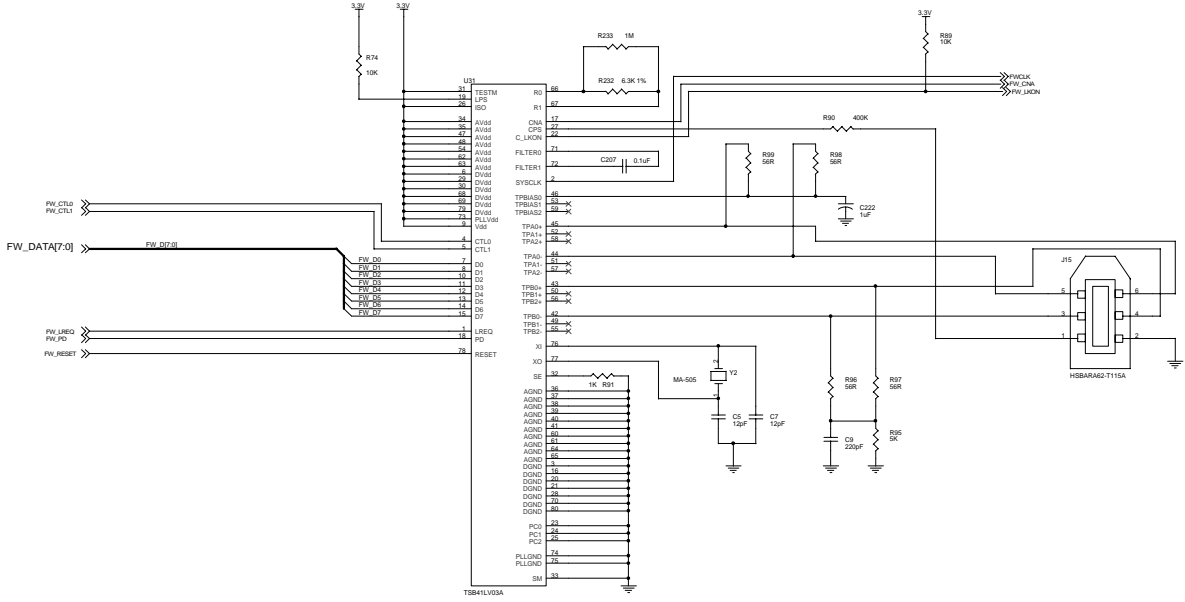
EPROM INTERFACE





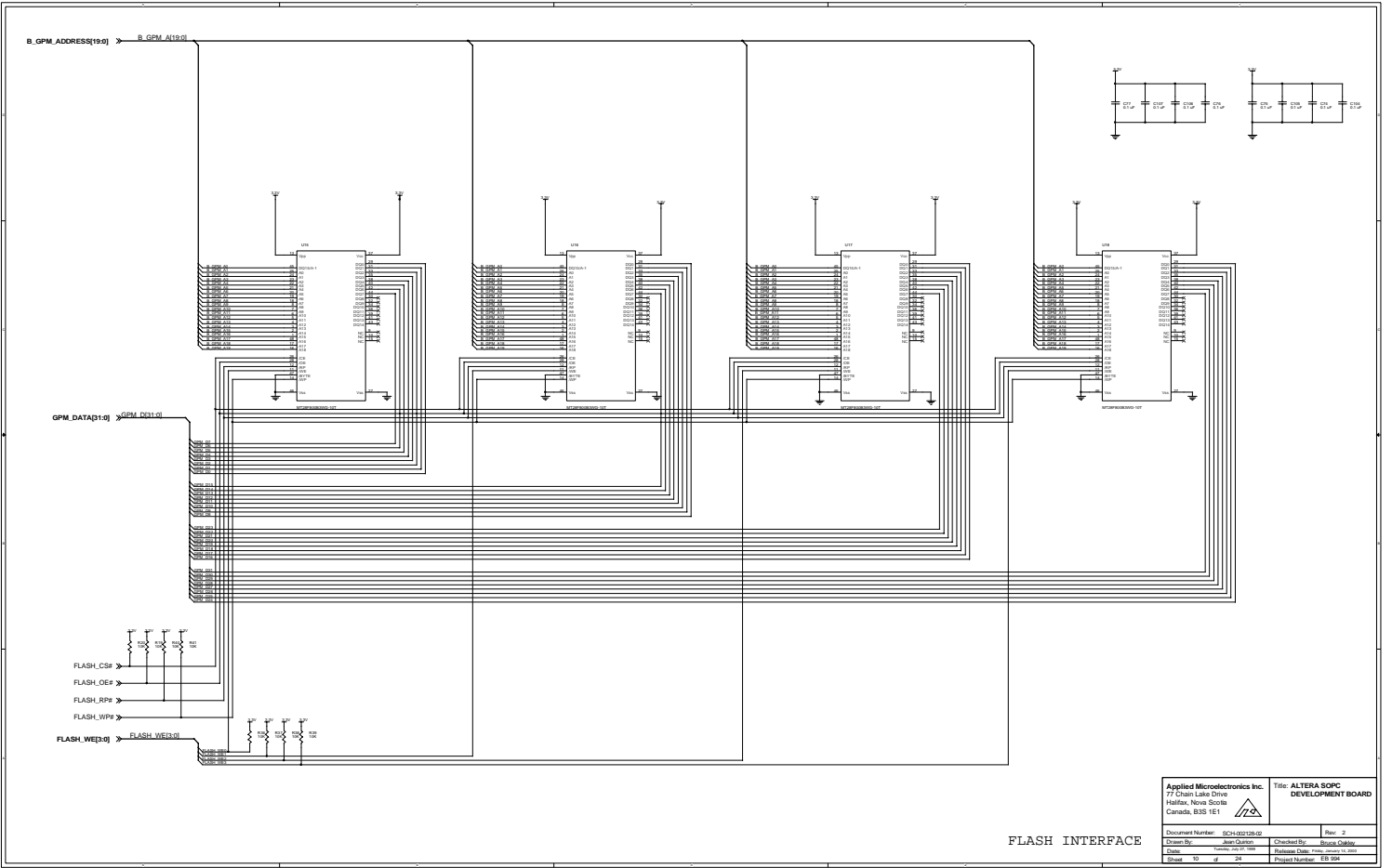
ETHERNET INTERFACE

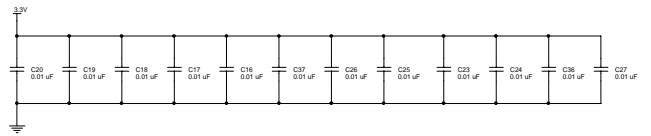
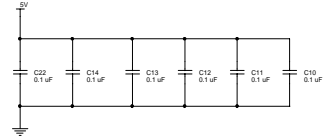
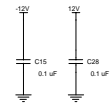
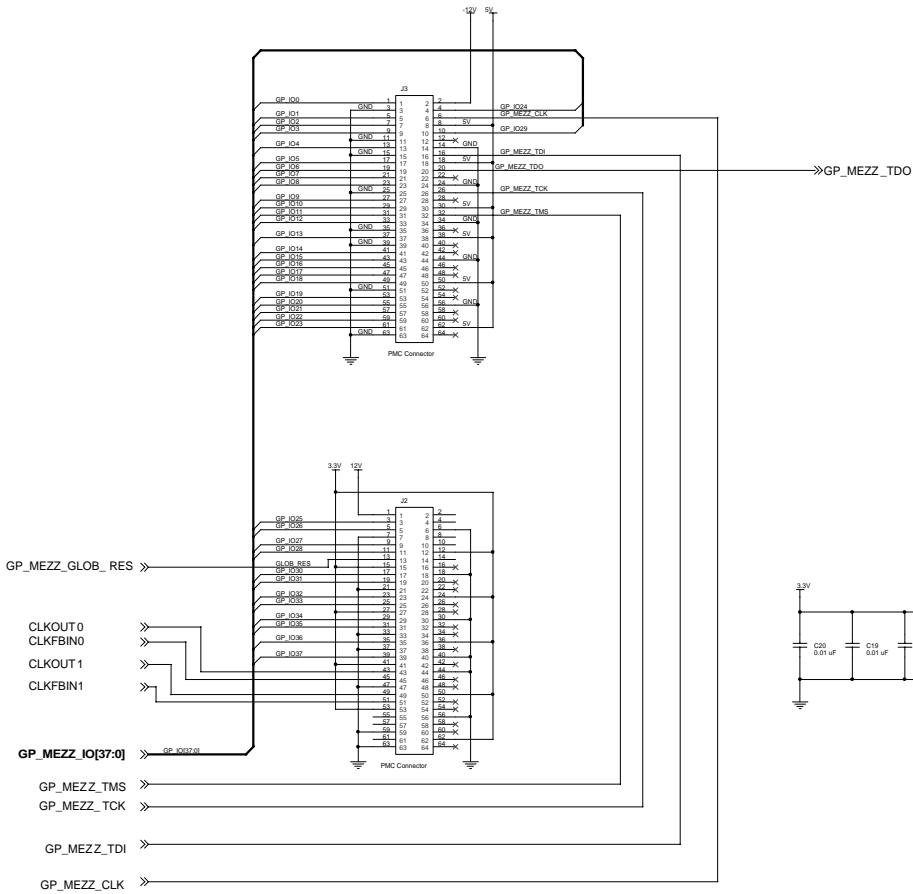
<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number:	SCH-002128-02	Rev:	1
Drawn By:	Jean Quirion	Checked By:	Bruce Oakley
Date:	Tuesday, July 27, 1999	Release Date:	Friday, January 14, 2000
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**FIREWIRE INTERFACE**

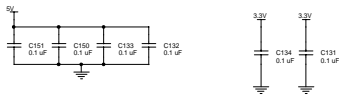
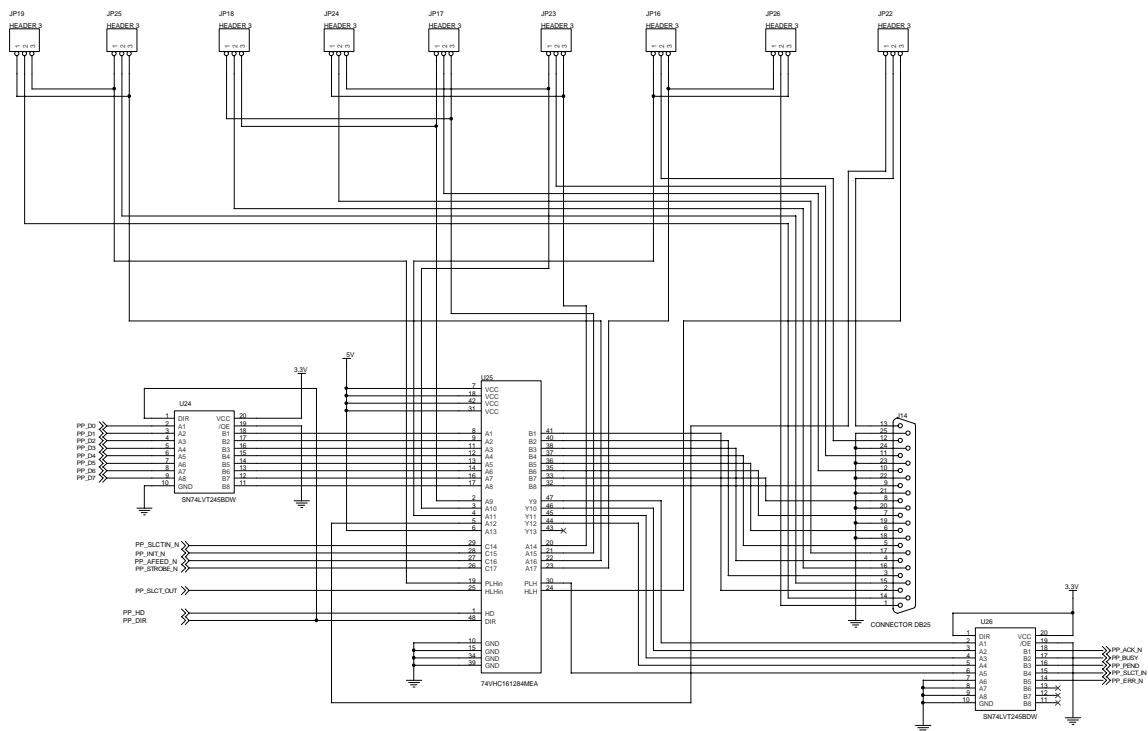
<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		Title: <b>ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number:	SCH-002128-02	Rev:	2
Drawn By:	Jean Quirion	Checked By:	Bruce Oakley
Date:	Tuesday, July 27, 1999	Release Date:	Friday, January 14, 2000
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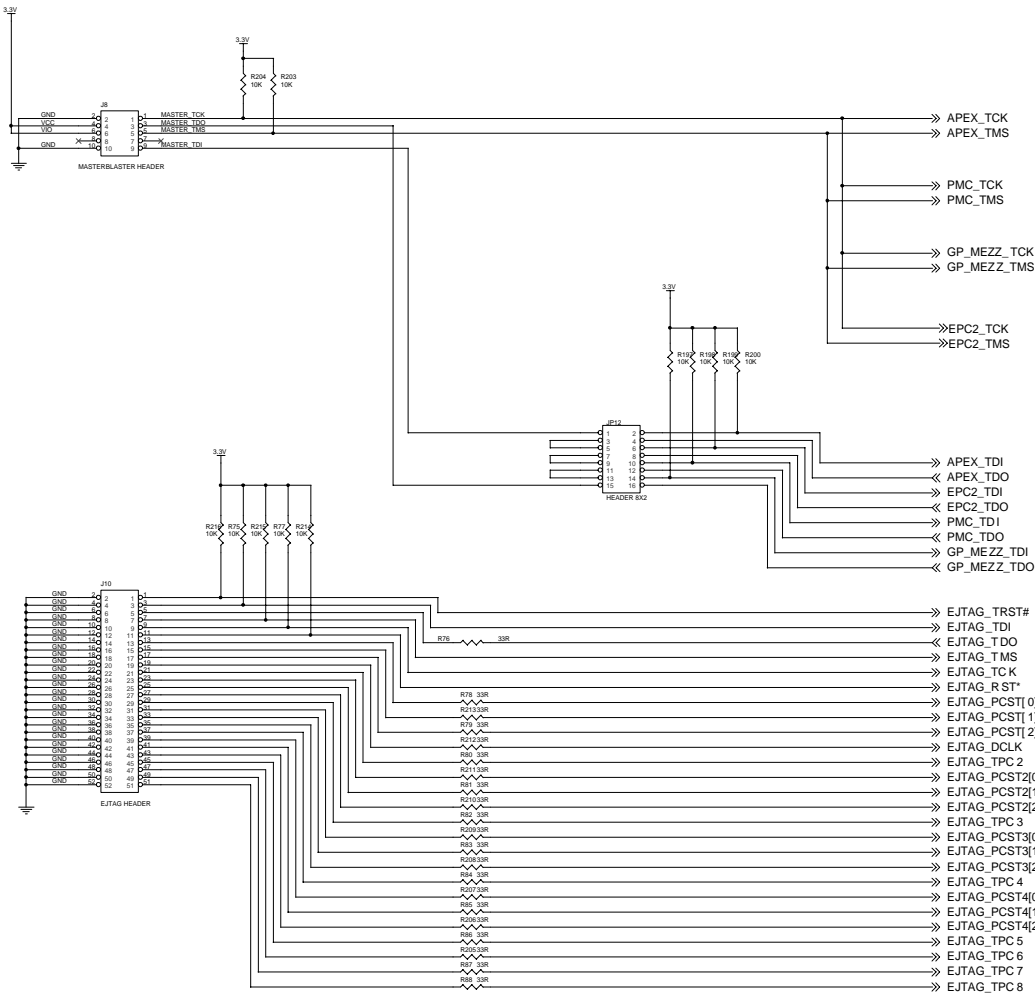
<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, 209 27, 1999	Release Date: Friday, January 14, 2000		
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GP\_MEZZANINE INTERFACE



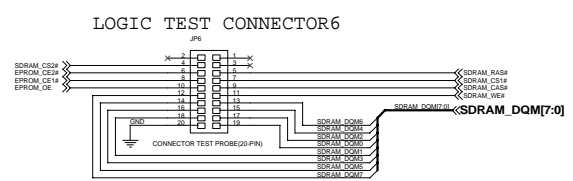
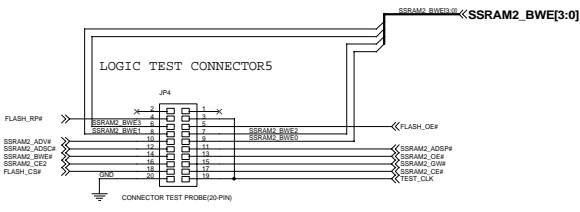
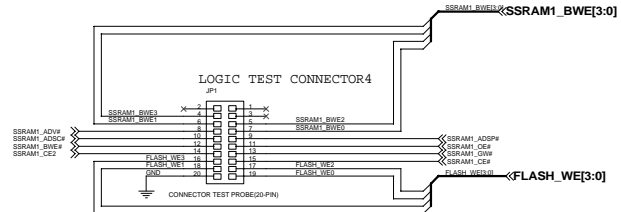
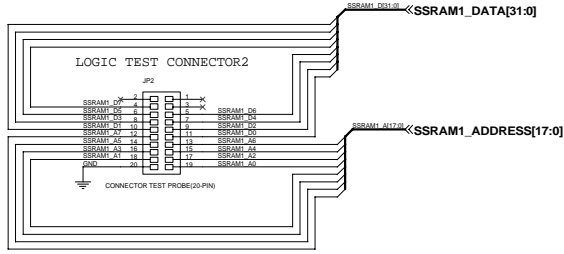
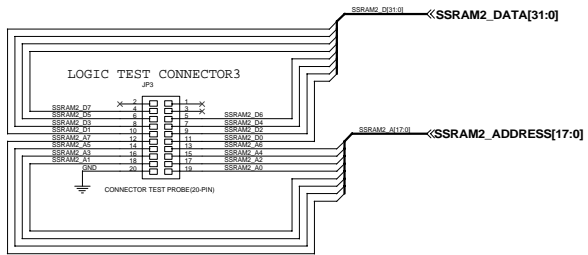
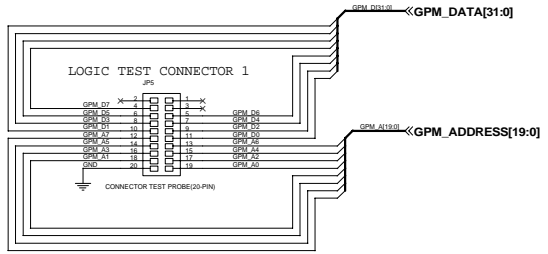
IEEE 1284 INTERFACE

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, 209 27, 1999	Release Date: Friday, January 14, 2000		
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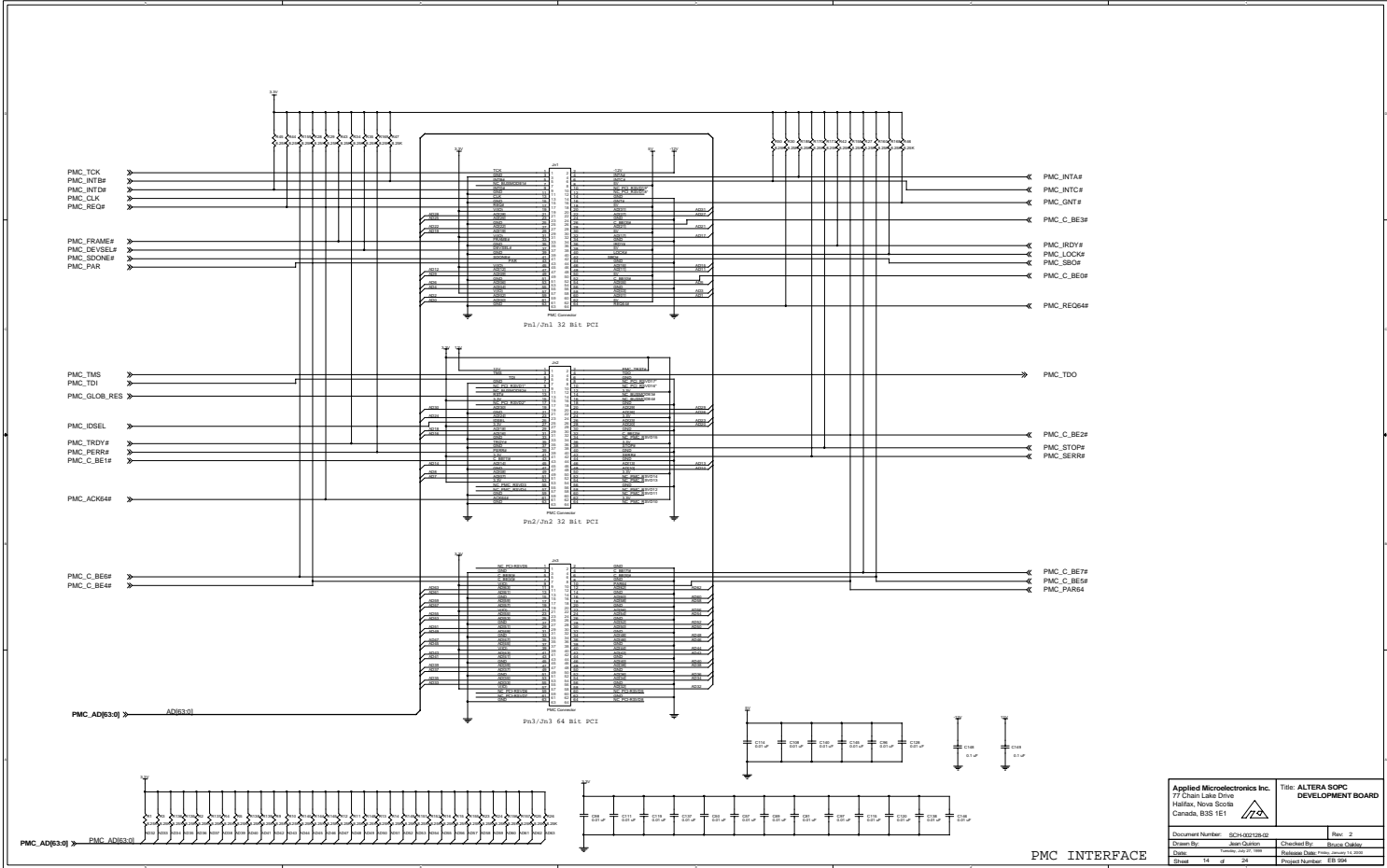
JTAG/EJTAG INTERFACE

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, 209 27, 1999	Release Date: Friday, January 14, 2000		
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<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02			Rev: 2
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, July 27, 1999	Release Date: Friday, January 14, 2000		
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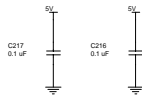
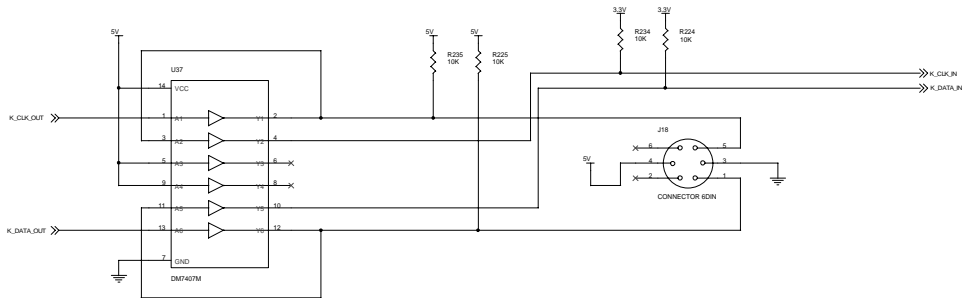
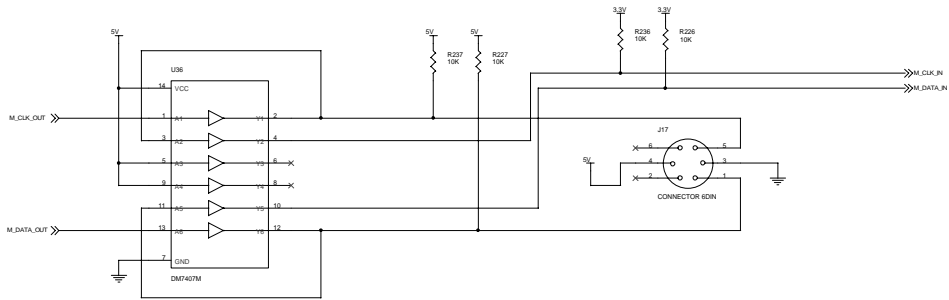
LOGIC ANALYZER INTERFACE



<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH402228-02	Rev: 2		
Drawn By: Alan O'Connell	Checked By: Bruce O'Leary		
Date: 06/01/2003	Release Date: 06/01/2003		
Sheet: 14	of 24	Project Number: 65196	

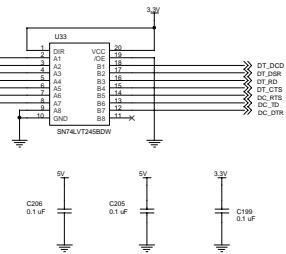
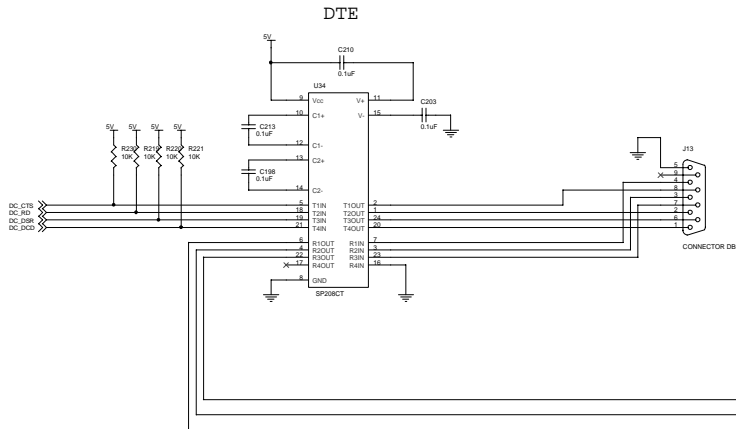
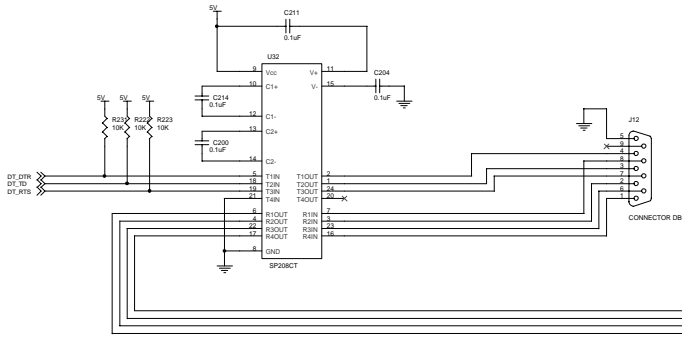
PMC INTERFACE





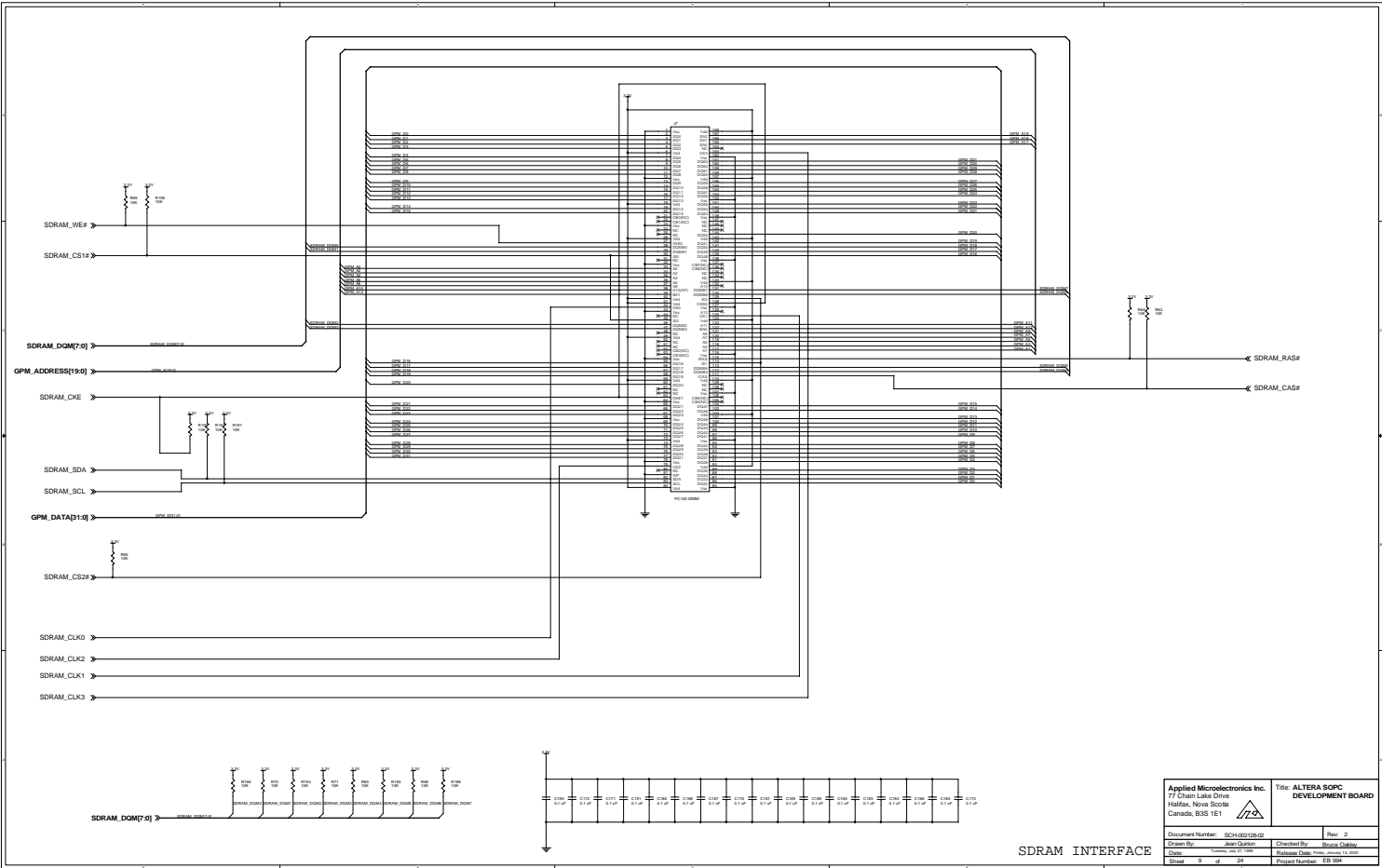
PS2 INTERFACE

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, July 27, 1999	Release Date: Friday, January 14, 2000		
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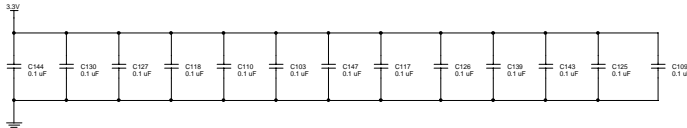
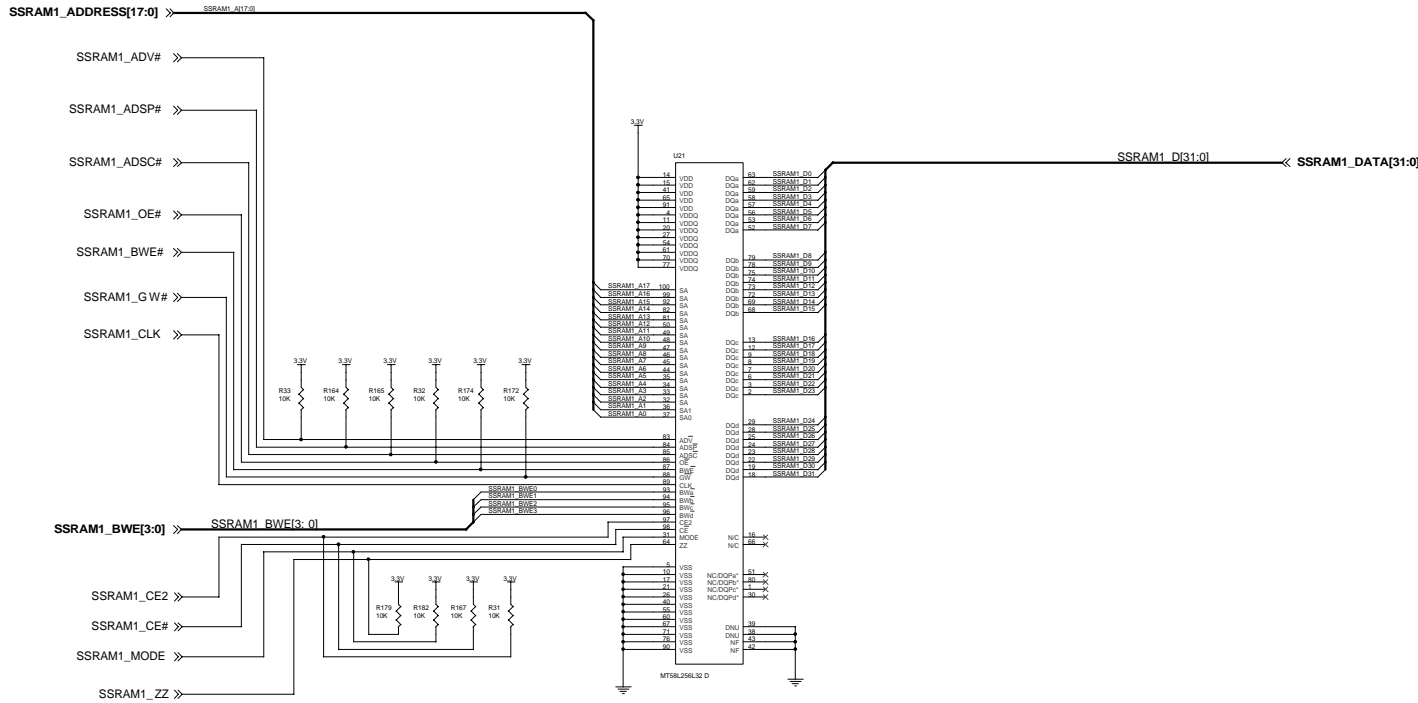


<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
Date: Tuesday, 209 27, 1999	Release Date: Friday, January 14, 2000		
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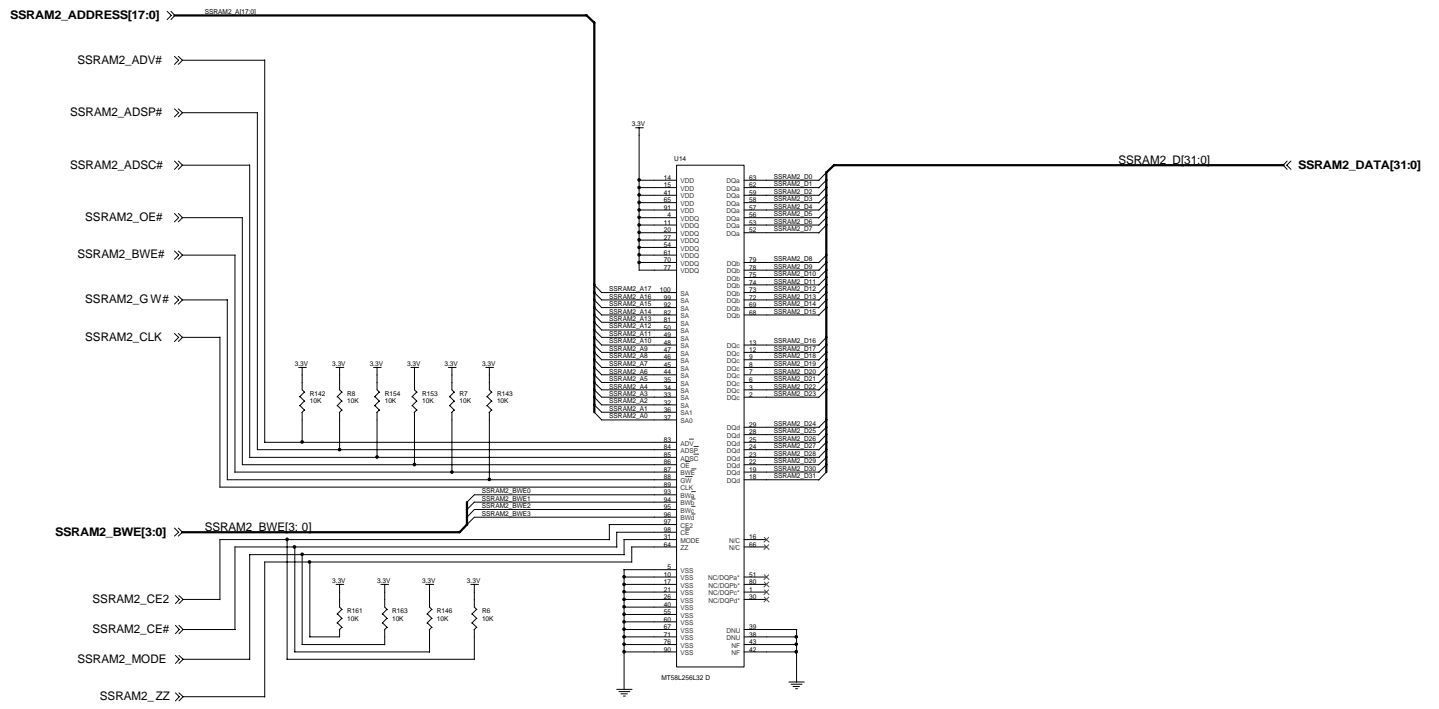
RS232 INTERFACE



<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number:	SCH402228-02	Rev:	2
Drawn By:	Jim Garton	Checked By:	Mark Oshby
Date:	11/11/00	Release Date:	11/11/00
Sheet:	9 of 24	Project Number:	EB 99A

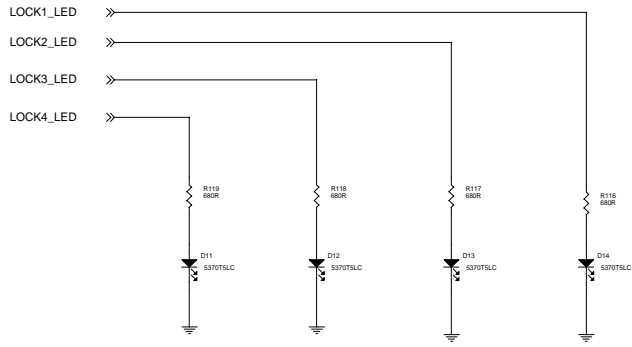


<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		Date: Tuesday, July 27, 1999
Sheet 12 of 24		Release Date: Friday, January 14, 2000	
Project Number: EB 994			

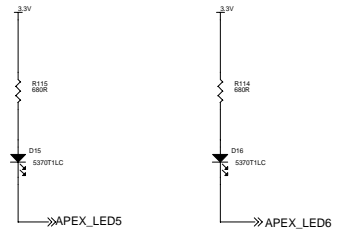


SSRAM INTERFACE 2

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		Date: Tuesday, July 27, 1999
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USER LEDS

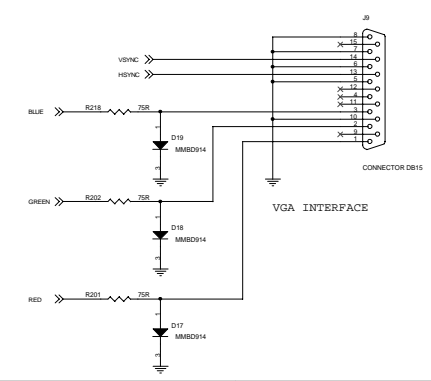
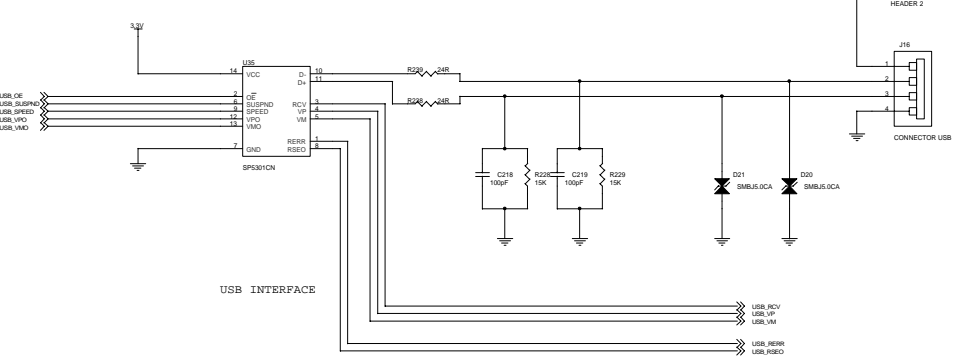
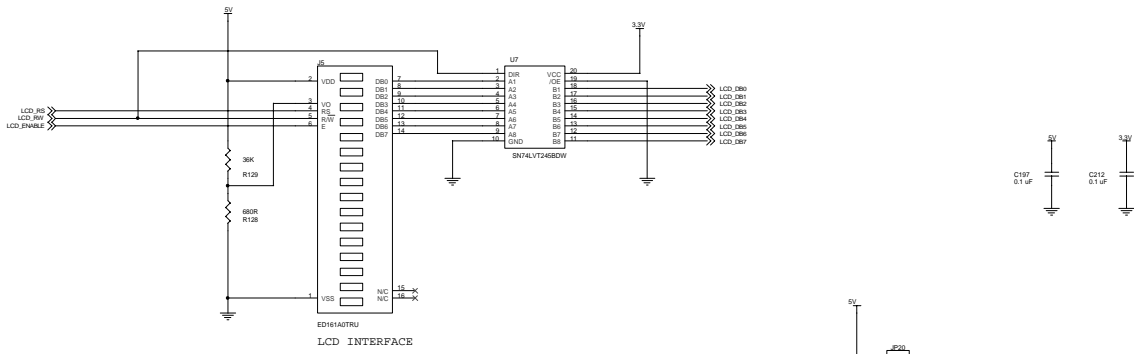


USER SWITCHES



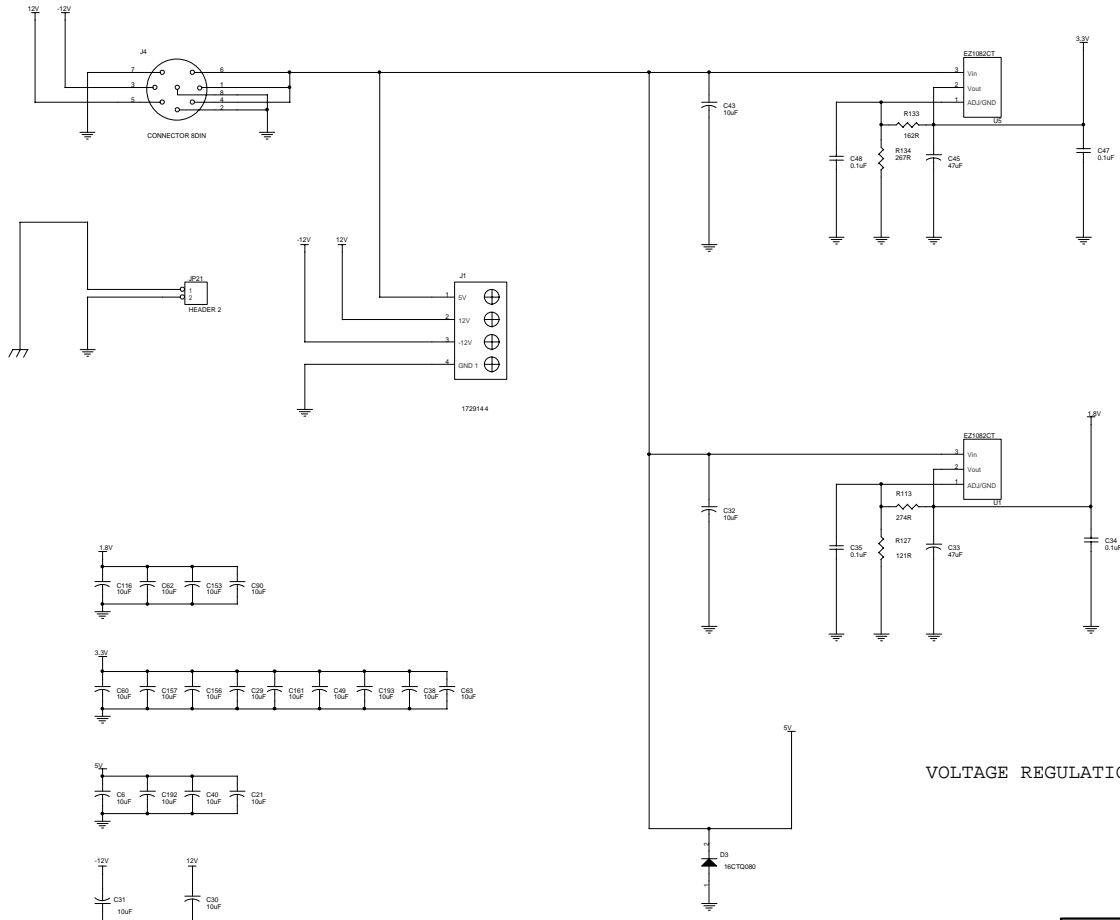
USER SWITCHES AND LEDS INTERFACE

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
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**LCD+USB+VGA INTERFACE**

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 2	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		
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VOLTAGE REGULATION

<b>Applied Microelectronics Inc.</b> 77 Chain Lake Drive Halifax, Nova Scotia Canada, B3S 1E1		<b>Title: ALTERA SOPC          DEVELOPMENT BOARD</b>	
Document Number: SCH-002128-02		Rev: 1	
Drawn By: Jean Quirion	Checked By: Bruce Oakley		Date: Tuesday, 20/27, 1999
Sheet 24 of 24		Release Date: Friday, January 14, 2000	
Project Number: EB 994			